



S&L-I0232

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Wolfgang Rösner et al.
Applic. No. : 10/047,013
Filed : January 16, 2002
Title : Method for Producing a Vertical
Semiconductor Transistor Component and
Vertical Semiconductor Transistor Component
Examiner : Fazli Erdem
Group Art Unit : 2826

ASSOCIATE POWER OF ATTORNEY

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

S i r :

Please recognize MANFRED BECK (Reg. No. 45,342) as my
associate in the matter of the above-identified application,
with full powers. Please continue addressing all
communications to the following address:

Lerner and Greenberg, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480

Respectfully submitted


For Applicants

Date: July 7, 2003

Lerner and Greenberg, P.A.
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Tel: (954) 925-1100
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A DOCPHOENIX

FOLLOW-ON DOCUMENT INDEX SHEET

INCOMING

____ ACPA ____
Continuing Prosecution Application

____ AP.B ____
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____ C680 ____
Request for Corrected Notice/Allowance

____ C.AD ____
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____ CFILE ____
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____ COCIN ____
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____ CRFD ____
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____ REM ____
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____ RESC ____
Rescind Non-Publication Request

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Specification Not in English